

IN THE SPECIFICATION:

Please amend paragraph [0040] as follows:

[0040] To restrict the locality of changes, the outputs names of the power domains are renamed and the original names are retained as outputs names of the isolation module. This ensures compatibility with signals feeding into other wakeup or "always on" domains. The synthesized netlist (or gate level netlist) may be produced by an IC synthesis tool. Synthesis tools produce a gate level netlist based on the RTL statements. One such synthesis tool is disclosed in a US patent application entitled "An Apparatus and Method for Handling of Multi-Level Circuit Design Data", serial number 10/118,242, now USP 6,993,733 to Murphy, issued on January 31, 2006, having a common assignee with the present invention and is hereby incorporated by reference.